

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Jae Soon KWON

Serial No.: 10/023,861

Group Art Unit: 1765

Filed: December 21, 2001

Examiner: Tran, Binh X.

For: METHOD OF SEMICONDUCTOR DEVICE ISOLATION

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PETITION FOR EXTENSION OF TIME

Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 13, 2004

Sir:

Applicant hereby petitions that an extension of the term necessary to render timely the filing of a response in the above-identified application be granted. Costs under 37 CFR 1.17(a)-(d) for such extension(s) and/or any other fee due with this response, not fully covered by the enclosed payment by credit card (Form PTO-2038) in the amount of \$110.00, may be charged to our Deposit Account No. 06-1358.

02/20/2004 TBESHAH1 00000041 10023861

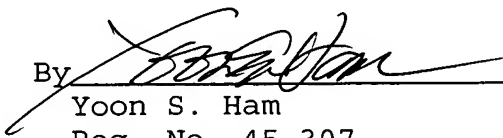
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Respectfully submitted,

JACOBSON HOLMAN, PLLC

By


Yoon S. Ham

Reg. No. 45,307

Direct Tel: (202)662-8483

400 Seventh Street, N.W.
Washington, D.C. 20004
Tel: (202)638-6666

Atty. Dkt. No.: P67464US0
YSH:dj